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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/911,409	07/25/2001	Tsuyoshi Tamura	110195	4925
25944	7590	04/20/2005	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			NGUYEN, KEVIN M	
			ART UNIT	PAPER NUMBER
			2674	
DATE MAILED: 04/20/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief	Application No. 09/911,409	Applicant(s) TAMURA, TSUYOSHI	
	Examiner Kevin M. Nguyen	Art Unit 2674	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 05 April 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
 b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
 (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
 (b) ☐ They raise the issue of new matter (see NOTE below);
 (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 (d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
 5. ☐ Applicant's reply has overcome the following rejection(s): _____.
 6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
 7. ☐ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
 The status of the claim(s) is (or will be) as follows:
 Claim(s) allowed: 3-8 and 11-16.
 Claim(s) objected to: _____.
 Claim(s) rejected: 1,2,9,10 and 17-25.
 Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
 9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
 10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
 12. ☒ Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). See continuation.
 13. ☒ Other: See Continuation Sheet.


XIAO WU
PRIMARY EXAMINER

Kevin M. Nguyen
 Patent Examiner
 Art Unit: 2674

Continuation of 11 NOTE: Applicant's arguments filed 04/05/2005 have been fully considered but they are not persuasive.

In response to applicant's argument that claims 1, 20, and 21 recite "a second control circuit that is independent of the first control circuit, that controls the reading and still image data or the moving-image data stored in the RAM, and that drives the display section." This argument is not persuasive because Kida et al teaches

The column electrode driver 35 separates the pixel drive data of one field that is read out (1) from the field memories 34A and 34B every bit, generates a pixel data pulse having a voltage value corresponding to each of the logical values "1" and "0" of the bit, and applies it to the column electrode of the plasma display panel (PDP) 36.

Various control signals such as write enable signal, read enable signal, and the like are supplied from a memory control circuit 37 to the field memories 34A and 34B.

The memory control circuit 37 (a first control circuit) controls addresses in the field memories 34A and 34B in accordance with an output signal from the control circuit 32 and controls the writing and reading operations in the field memories 34A and 34B (2) of the pixel data that is supplied from the image data processing circuit 33.

Operating means 38 sets and resets a motion image/still image mixture mode (3) which enables a motion image and a still image to be simultaneously displayed on the screen and supplies a motion image/still image mixture mode designation signal to the control circuit 32 (col. 10, line 59 through col. 11, lines 11).

Therefore, the teaching of Kido et al's reference provides and establishes the "substantial evidence" to produce and result the claimed limitation as follow: a second control circuit (the column driver 35) that is independent of the first control circuit (the control circuit 37 controls operation means 38), that controls the reading (1) and still image data or the moving-image data (3) stored in the RAM (2), and that drives (the column driver 35) the display section (the plasma display panel (PDP) 36).

For these reasons, the rejections based on Kido et al have been maintained.

Continuation of 12 NOTE: The information disclosure statement filed 01/14/2005 which has been considered as to the merits.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Hiroyuki et al (JP 09-281933) discloses a memory-incorporated driver (307) storing a still-frame picture data and a moving-frame picture data that are sent by the selector (the ports) that are from an external CPU 304 (external MPU) (see abstract).

Continuation of 13 NOTE: The terminal disclaimer filed on 04/05/2005 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of 09/911,409 has been reviewed and is accepted. The terminal disclaimer has been recorded.